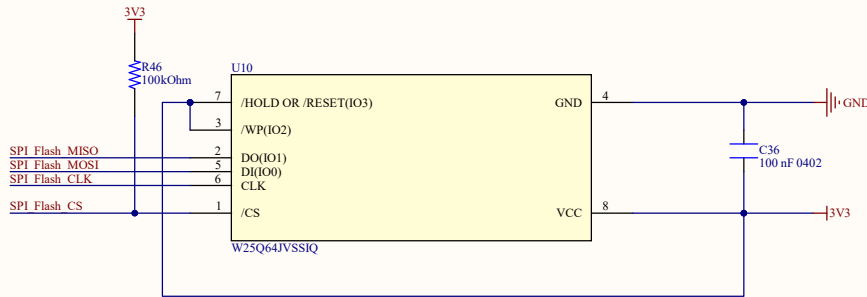
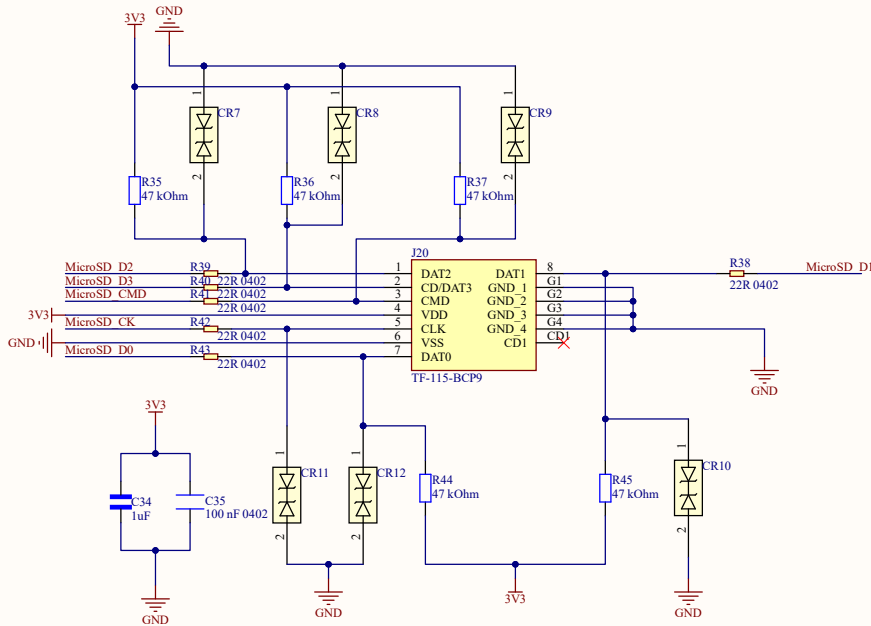


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Matched Net Lengths [Tol = 100 mil]

STM32H7 Board[1A]	MicroSD D3	MicroSD D3
STM32H7 Board[1A]	MicroSD D2	MicroSD D2
STM32H7 Board[1A]	MicroSD D1	MicroSD D1
STM32H7 Board[1A]	MicroSD D0	MicroSD D0
STM32H7 Board[1A]	MicroSD CK	MicroSD CMD
STM32H7 Board[1A]	MicroSD CMD	

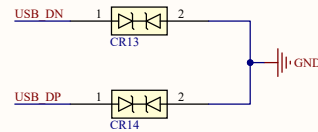
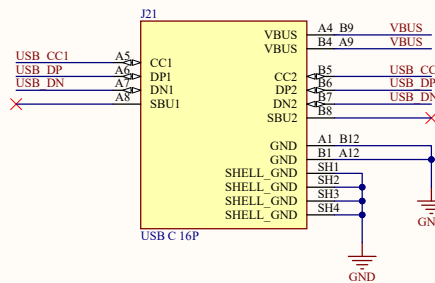
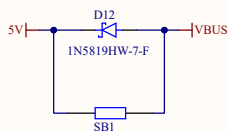
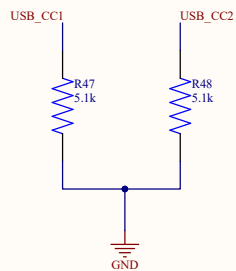
STM32H7 Board[1A]	SPI MISO	SPI Flash MISO
STM32H7 Board[1A]	SPI MOSI	SPI Flash MOSI
STM32H7 Board[1A]	SPI SCK	SPI Flash CLK
STM32H7 Board[1A]	SPI CS	SPI Flash CS



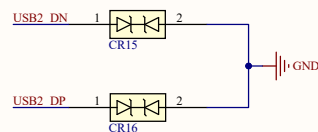
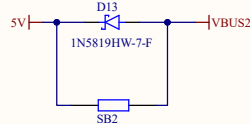
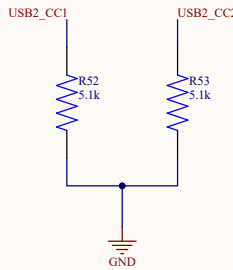
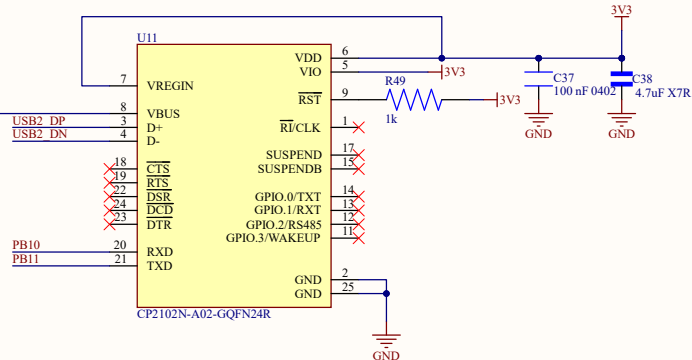
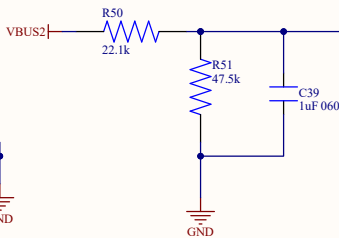
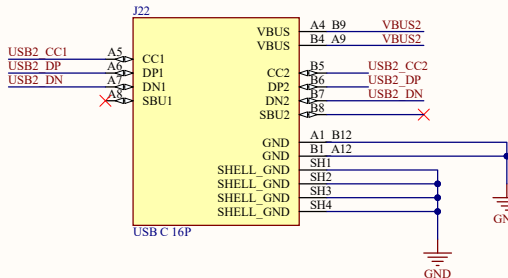
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Size B	Number 03	Revision 04
Date: 7/23/2026	Sheet of Neural Nexus	
File: C:\Work\Atrium\EDR\MicroSD.SchDoc	Drawn By: Dulana Pitrwaduge	

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STM32H7 Board[1A] USB1_DN USB_DN
STM32H7 Board[1A] USB1_DP USB_DP



STM32H7 Board[1A] USB2_TX PB10
STM32H7 Board[1A] USB2_RX PB11



Title: PI_CON		
Size B	Number 04	Revision 01
Date: 7/23/2026	Sheet of Neural Nexus	
File: C:\Work\Altium\EDR\PI_CON.SchDoc	Drawn By: Dulana Pritwaduge	